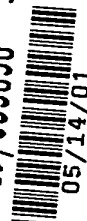




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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEPATENT APPLICATION
TRANSMITTAL LETTER
UNDER 37 C.F.R. 1.53(b)ATTORNEY DOCKET NO.:
2207/10551Assistant Commissioner
for Patents
Washington D.C. 20231
Box Patent Application

Transmitted herewith for filing is a patent application.

Inventor(s): **Samie B. Samaan, Paul Madland and Gary L. Woods**For: A DEVICE AND METHOD FOR PROBING INSTANTANEOUS HIGH-SPEED
LOCAL SUPPLY VOLTAGE FLUCTUATION IN VLSI INTEGRATED CIRCUITS
USING IR EMISSIONS

Enclosed are:

1. 9 sheet(s) of specification, 4 sheet(s) of claims, and 1 sheet of abstract.
2. 7 sheet(s) of drawings, ~~also enclosed~~ are: Assignment, Signed declaration and IDS, 1449 & Refs.
3. The filing fee has been calculated as shown below:

	NUMBER FILED	NUMBER EXTRA*	RATE (\$)	FEE (\$)
BASIC FEE				710.00
TOTAL CLAIMS	18 - 20 =	0	18.00	0.00
INDEPENDENT CLAIMS	4 - 3 =	1	80.00	80.00
MULTIPLE DEPENDENT CLAIM PRESENT			270.00	0
*Number extra must be zero or larger			TOTAL	790.00
If the applicant is a small entity under 37 C.F.R. §§ 1.9 and 1.27, then divide total fee by 2, and enter amount here.			SMALL ENTITY TOTAL	

4. Please charge the required application filing fee of **\$790.00** to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**.

5. The Commissioner is hereby authorized to charge payment of the following fees, associated with this communication or arising during the pendency of this application, or to credit any overpayment, to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**:

- A. Any additional filing fees required under 37 C.F.R. § 1.16;
- B. Any additional patent application processing fees under 37 C.F.R. § 1.17;
- C. Any additional document supply fees under 37 C.F.R. § 1.19;
- D. Any additional post-patent processing fees under 37 C.F.R. § 1.20; or
- E. Any additional miscellaneous fees under 37 C.F.R. § 1.21.

6. A duplicate copy of this letter is enclosed for that purpose.

Respectfully submitted,

Dated: 5/14/01

By: 

Gerard A. Messina
Reg. No. 35,952

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DATE OF DEPOSIT 5/14/01

TYPE OF DOCUMENT Patent Application

re: Saman et al

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A device and method for
probing instantaneous high-speed
local supply voltage fluctuations
in VLSI integrated circuits using IR
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